

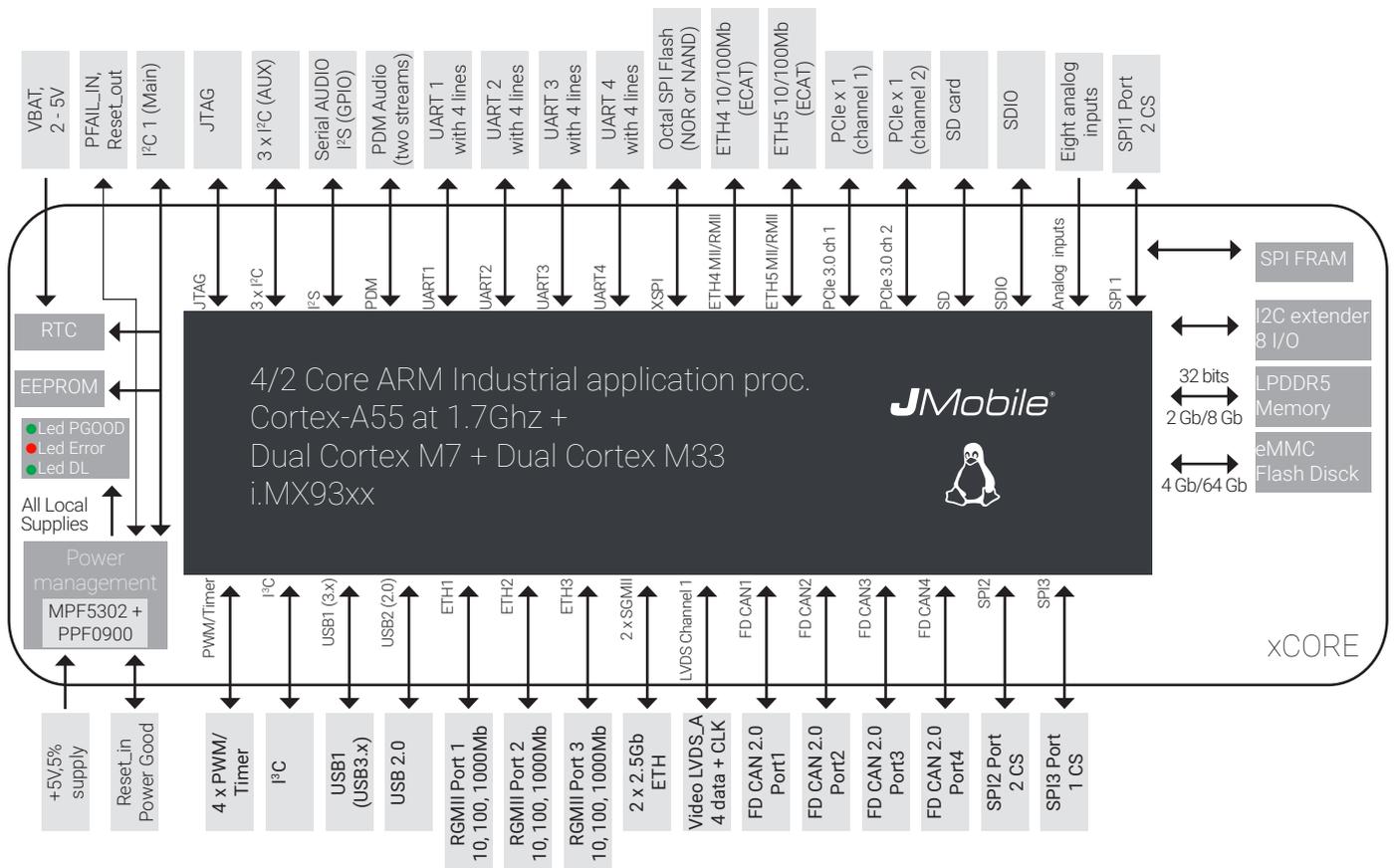
# xCORE - Preliminary Technical Data



|                   |  |
|-------------------|--|
| CPU               | i.MX943 Quad Cores ARM Cortex-A55 at 1.7 Ghz + Dual Cortex ARM Cortex M7 + Dual Cores ARM Cortex M33 |
| DDR               | Up to 8 GByte LPDDR5 x32 bit   |
| Flash Disk        | Up to 64 GByte eMMC x8bit  |
| EEPROM            | 4 Kbits  |
| LEDS              | 1x GREEN, 1x RED   |
| FRAM              | 64-Kbytes  |
| RTC               | Yes  |
| USB               | 1x OTG 2.0, 1x Host 3.x  |
| Ethernet          | 3x RGMII ports 10/100/1000, 2x SGMII 1G/2.5G   |
| SD                | 1  |
| SDIO              | 1  |
| SPI               | 3  |
| PCIe              | 2x PCIe Gen 3.0 x1   |
| I2C               | 3  |
| I3C               | 1  |
| CAN               | 3x CAN FD / CAN 2.0  |
| UART              | 4x UARTS (4 lines)   |
| Audio             | 1x I2S Channel, 2x PDM streams   |
| Video Output      | 1x LVDS Single Channel   |
| GPIO              | Power-Fail, Power Good, Reset IN, Reset Out functions.   |
| Analog input      | 8 single ended channels, 12bits, 1V8 Max   |
| Security features | EdgeLock Secure Enclave  |
| JTAG              | Yes  |
| Power Supply      | Single 5,0 Volt / VBB for RTC Backup   |
| Dimensions        | 56x41.2 mm   |
| Temperature Range | -40°C to +85°C   |
| Connector         | 250 plated half holes, 0,7mm pitch - 138 bottom plate pads, 1mm pitch                                |

xCORE SOM is the new Software-Defined SOM built on the NXP i.MX943 processor. It combines real-time performance, secure-by-design architecture, and edge-to-cloud integration, enabling OEMs to develop next-generation industrial platforms faster and with greater scalability.

Software configurable. Not all selections can be combined



## Ordering Information

| Model | Part Number   | Description  |
|-------|---------------|--|
| xCORE | To be Defined | xCORE: i.MX943, Up to 8 GByte LPDDR5 x32 bit, -40 to +85°C |